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#3 *Frederick P. Fish*
5-14-01

FISH RICHARDSON P.C.

01/30/01
1c984 U.S. PTOFrederick P. Fish
1855-1930
W.K. Richardson
1859-1951

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1c986 U.S. PTO
09/774427
01/30/014350 La Jolla Village Drive
Suite 500
San Diego, California
92122Telephone
858 678-5070Facsimile
858 678-5099Web Site
www.fr.com

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Box Patent Application
Commissioner for Patents
Washington, DC 20231

Presented for filing is a new continuation patent application of:

Applicant: SHUNPEI YAMAZAKI AND TAKESHI FUKUNAGA

Title: SEMICONDUCTOR DEVICES AND MANUFACTURING METHODS
THEREOFEnclosed are the following papers, including those required to receive a filing date
under 37 CFR §1.53(b):

	<u>Pages</u>
Specification	29
Claims	4
Abstract	1
Declaration	2 (copy from parent)
Drawing(s)	7

Enclosures:

- Rule 62 declaration, copy from a previous application under rule 63(d) for continuation or divisional only.
- Information Disclosure Statement: Applicants call attention to documents listed on attached form(s) PTO-892 and PTO-1449 from the parent case. Per Rule 97(d), copies of those documents are not provided.
- Postcard.

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I hereby certify under 37 CFR §1.10 that this correspondence is being deposited with the United States Postal Service as Express Mail Post Office to Addressee with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

Date of Deposit January 30, 2001

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Gildardo Vargas

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